Dkt: 884.A53US1 (INTEL)

Title: SYSTEM AND METHOD FOR CHANNELIZATION RECOGNITION IN A WIDEBAND COMMUNICATION SYSTEM

Assignee: Intel Corporation

REMARKS

The amendments to the specification reflect changes made to originally filed FIG. 7, which now has become FIGs. 7A, 7B, 7C and 7D. No new matter has been added by the amendment to the specification.

No claims have been amended, cancelled, or added. Claims 1-30 are currently pending.

It is believed that the claims are in condition for examination and notification to that effect is respectfully requested.

The Examiner is invited to contact Applicant's representative at (612) 349-9592 if prosecution may be assisted thereby.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully Submitted,

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Date May 10, 2004

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CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this Day 2004.

Name Chris Damagno

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